



## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**Complete if Known**

<b>Application Number</b>	Submitted Herewith
<b>Filing Date</b>	Submitted Herewith
<b>First Named Inventor</b>	Jessen, Scott
<b>Group Art Unit</b>	Not Yet Assigned
<b>Examiner Name</b>	Not Yet Assigned
<b>Attorney Docket Number</b>	Jessen 7-1-4/075903-023

U1050 U.S. PTO  
09/966157  
09/28/01

Examiner  
Initials:Cite  
No.<sup>1</sup>

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

 $T^2$ 

S. WOLF and R.N. TAUBER, *Silicon Processing for VLSI Era, Volume 1 – Process Technology*, pp. 797-801, Second Edition (2000), Published by Lattice Press, Sunset Beach, California

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.